



FEB. 01 2007

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NO. 549 P. 2

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Monique Lawson	(Depositor's name)
<i>Monique Lawson</i>	(Signature)
February 1, 2007	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/799,061	03/12/2004	Eitan Cadouri	524322001200	6747

TITLE OF INVENTION: SELECTING DIE PLACEMENT ON A SEMICONDUCTOR WAFER TO REDUCE TEST TIME

APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	YES	\$700	\$0	\$0	\$700	02/02/2007
EXAMINER		ART UNIT	CLASS-SUBCLASS			
HOLLINGTON, JEROME M		2829	324-756000			

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).
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PDF Solutions, Inc.

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

San Jose, California

Please check the appropriate assignee category or categories (will not be printed on the patent): Individual Corporation or other private group entity Government

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 a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(b)(2).

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Authorized Signature Peter J. Yim

Date February 1, 2007

Typed or printed name Peter J. Yim

Registration No. 44,417

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FROM: Peter J. Yim
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DATE: February 1, 2007

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U.S. Patent Application Serial No. 10/799,061
For: SELECTING DIE PLACEMENT ON A SEMICONDUCTOR WAFER TO
REDUCE TEST TIME
By: Eitan CADOURI
Our Reference: 52432-20012.00

Attached Is The Following:

PTOL-85 (in duplicate, 2 pages)

sf-1910334